

bk Nov 8-14 1994.log

RCS file: /p/cvsroot/calliope/verilog/src/ci/ci_digital.v,v
Working file: calliope/verilog/src/ci/ci_digital.v
head: 6.48
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 48; selected revisions: 1
description:

revision 6.48
date: 1994/11/10 18:34:02; author: agc; state: Exp; lines: +5 -2
additional scaling modes for accumulator
=====

RCS file: /p/cvsroot/calliope/verilog/src/ci/ci_file_control.pim,v
Working file: calliope/verilog/src/ci/ci_file_control.pim
head: 43.3
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 3; selected revisions: 1
description:

revision 43.3
date: 1994/11/10 18:34:04; author: agc; state: Exp; lines: +20 -19
additional scaling modes for accumulator
=====

RCS file: /p/cvsroot/calliope/verilog/src/ci/ci_fir.v,v
Working file: calliope/verilog/src/ci/ci_fir.v
head: 6.22
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 22; selected revisions: 1
description:

revision 6.22
date: 1994/11/10 18:34:06; author: agc; state: Exp; lines: +8 -1
additional scaling modes for accumulator
=====

RCS file: /p/cvsroot/calliope/verilog/src/ci/ci_round10x12.pim,v
Working file: calliope/verilog/src/ci/ci_round10x12.pim
head: 43.2
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 2; selected revisions: 1
description:

revision 43.2
date: 1994/11/10 18:34:08; author: agc; state: Exp; lines: +10 -9
additional scaling modes for accumulator

```
=====
RCS file: /p/cvsroot/calliope/verilog/src/ci/pimlib.pl,v
Working file: calliope/verilog/src/ci/pimlib.pl
head: 19.41
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 41;    selected revisions: 1
description:
-----
```

```
revision 19.41
date: 1994/11/10 18:34:11; author: agc; state: Exp; lines: +2 -1
    additional scaling modes for accumulator
=====
```

```
=====
RCS file: /p/cvsroot/calliope/verilog/src/eq/eq.v,v
Working file: calliope/verilog/src/eq/eq.v
head: 1.29
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 29;    selected revisions: 1
description:
-----
```

```
revision 1.29
date: 1994/11/10 18:32:55; author: agc; state: Exp; lines: +15 -4
    additional accumulator scaling modes, redefines fields in device
    registers
=====
```

```
=====
RCS file: /p/cvsroot/calliope/verilog/src/eq/eq.h,v
Working file: calliope/verilog/src/eq/eq.h
head: 1.9
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 9;    selected revisions: 1
description:
-----
```

```
revision 1.9
date: 1994/11/10 18:32:58; author: agc; state: Exp; lines: +5 -4
    additional accumulator scaling modes, redefines fields in device
    registers
=====
```

```
=====
RCS file: /p/cvsroot/calliope/verilog/src/eq/pimlib.pl,v
Working file: calliope/verilog/src/eq/pimlib.pl
head: 7.32
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 32;    selected revisions: 1
description:
-----
```

bk Nov 8-14 1994.log

revision 7.32

date: 1994/11/10 18:33:00; author: agc; state: Exp; lines: +3 -1
additional accumulator scaling modes, redefines fields in device
registers

=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/Makefile,v

working file: calliope/verilog/src/fp/Makefile

head: 1.60

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 60; selected revisions: 1

description:

local makefile

revision 1.60

date: 1994/11/10 18:30:54; author: agc; state: Exp; lines: +3 -1
support for additional accumulator scaling modes.

=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_accmpy.v,v

working file: calliope/verilog/src/fp/fp_accmpy.v

head: 1.21

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 21; selected revisions: 1

description:

four

revision 1.20

date: 1994/11/10 18:30:57; author: agc; state: Exp; lines: +23 -22
support for additional accumulator scaling modes.

=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_cmac.v,v

working file: calliope/verilog/src/fp/fp_cmac.v

head: 1.8

branch:

locks: strict

access list:

symbolic names:

keyword substitution: kv

total revisions: 8; selected revisions: 1

description:

five

revision 1.8

date: 1994/11/10 18:30:59; author: agc; state: Exp; lines: +9 -3
support for additional accumulator scaling modes.

=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_cmpy.v,v

working file: calliope/verilog/src/fp/fp_cmpy.v

head: 1.11

branch:

locks: strict

access list:

bk Nov 8-14 1994.log

symbolic names:
keyword substitution: kv
total revisions: 11; selected revisions: 1
description:
six

revision 1.11
date: 1994/11/10 18:31:01; author: agc; state: Exp; lines: +14 -13
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_fft_12.v,v
Working file: calliope/verilog/src/fp/fp_fft_12.v
head: 1.11
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 11; selected revisions: 1
description:
two

revision 1.11
date: 1994/11/10 18:31:03; author: agc; state: Exp; lines: +3 -2
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_mpy10x12.v,v
Working file: calliope/verilog/src/fp/fp_mpy10x12.v
head: 3.8
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 8; selected revisions: 1
description:

revision 3.8
date: 1994/11/10 18:31:04; author: agc; state: Exp; lines: +3 -2
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_round10x12.v,v
Working file: calliope/verilog/src/fp/fp_round10x12.v
head: 3.13
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 13; selected revisions: 1
description:

revision 3.13
date: 1994/11/10 18:31:06; author: agc; state: Exp; lines: +20 -18
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_round_10.v,v
Working file: calliope/verilog/src/fp/fp_round_10.v
head: 2.4

bk Nov 8-14 1994.log

branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 4; selected revisions: 1
description:
roundr

revision 2.4
date: 1994/11/10 18:31:08; author: agc; state: Exp; lines: +13 -12
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_sa.v,v
Working file: calliope/verilog/src/fp/fp_sa.v
head: 32.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 32.1
date: 1994/11/10 18:31:10; author: agc; state: Exp;
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_ta.v,v
Working file: calliope/verilog/src/fp/fp_ta.v
head: 32.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 32.1
date: 1994/11/10 18:31:11; author: agc; state: Exp;
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/fp_tmac10x12.v,v
Working file: calliope/verilog/src/fp/fp_tmac10x12.v
head: 3.6
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 6; selected revisions: 1
description:

revision 3.6
date: 1994/11/10 18:31:13; author: agc; state: Exp; lines: +9 -4
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/fp/pimlib.pl,v

bk Nov 8-14 1994.log
working file: calliope/verilog/src/fp/pimlib.pl

head: 11.41
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 41; selected revisions: 1
description:

revision 11.41
date: 1994/11/10 18:31:15; author: agc; state: Exp; lines: +14 -2
support for additional accumulator scaling modes.
=====

RCS file: /p/cvsroot/calliope/verilog/src/hc/hc.ut,v
working file: calliope/verilog/src/hc/hc.ut

head: 40.1
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 1; selected revisions: 1
description:

revision 40.1
date: 1994/11/11 19:05:24; author: lisar; state: Exp;
undertow file for hc debug.
=====

RCS file: /p/cvsroot/calliope/verilog/src/pp/pp_outgoing_fifo_10.v,v
working file: calliope/verilog/src/pp/pp_outgoing_fifo_10.v

head: 1.16
branch:
locks: strict
access list:
symbolic names:
keyword substitution: kv
total revisions: 16; selected revisions: 1
description:
8/10-bit data return

revision 1.16
date: 1994/11/10 18:32:04; author: agc; state: Exp; lines: +3 -3
adjust output packing to accept data left adjusted rather
than right adjusted
=====